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# Polymer PTC Devices

Surface mount fuses

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## LP-NSM075

## Features

- □ Very small size of 1206
- □ Fast tripping resettable circuit protection
- $\hfill \Box \qquad {\sf Surface mount packaging for automated assembly}$
- Agency recognition: UL、CSA、TUV Mus

### Product Dimensions (mm)

Max M	Min.
).60 0.	0.20
_	0.60



## **Electrical Characteristics**

Part number	I <sub>H</sub>	Ι <sub>Τ</sub>	V <sub>max</sub>	I <sub>max</sub>	T <sub>trip</sub>	<b>)</b>	Pd <sub>typ</sub>	R <sub>min</sub>	$R_{1max}$
	(A)	(A)	(V)	(A)	Current(A)	Time(S)	(W)	()	()
LP-NSM075	0.75	1.50	6	40	8.0	0.20	0.6	0.10	0.29

 $I_{H}$ =Hold current: maximum current at which the device will not trip at 25 still air.

 $I_{T} = \mbox{Trip current: minimum current at which the device will always trip at 25 \qquad \mbox{still air.}$ 

 $V_{\text{max}} \!\!=\!\! Maximum$  voltage device can withstand without damage at rated current.

 $I_{\text{max}} = Maximum$  fault current device can withstand without damage at rated voltage.

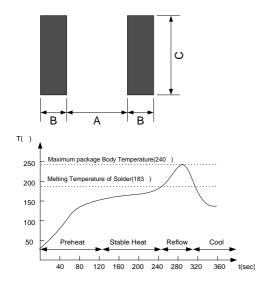
T<sub>trip</sub>=Maximum time to trip(s) at assigned current.

Pd<sub>typ</sub>=Typical power dissipation: typical amount of power dissipated by the device when in state air environment.

R<sub>min</sub>=Minimum device resistance at 25 prior to tripping.

R1max=Maximum device resistance measured in the nontripped state 1 hour post reflow.

### Solder Reflow Recommendations



### **Solder Pad Layouts**

Part number	Α	В	С
Fait number	(mm)	(mm)	(mm)
LP-NSM075	1.80	1.00	1.80

\* Recommended reflow methods: IR, Vapor phase oven, hot air oven, wave solder.

\* Devices can be cleaned using standard industry methods and solvents.

#### Notes:

If reflow temperatures exceed the recommended profile, devices may not meet the performance requirements.

#### **Package Information**

Tape & Reel: 3000pcs per reel.

